

FIG. 1

FOR INFORMATION

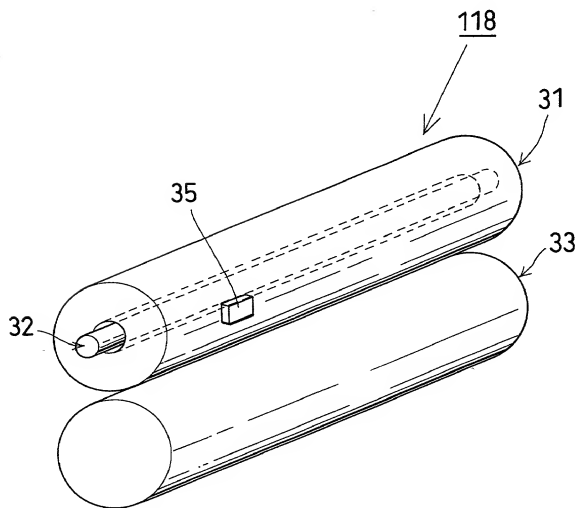


FIG. 2

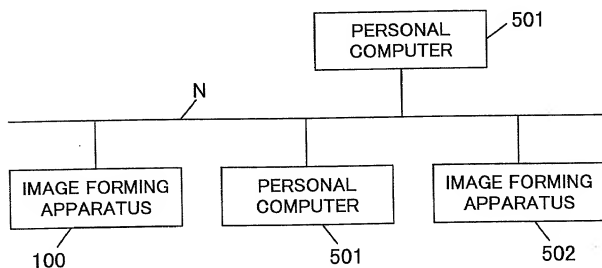


FIG. 3

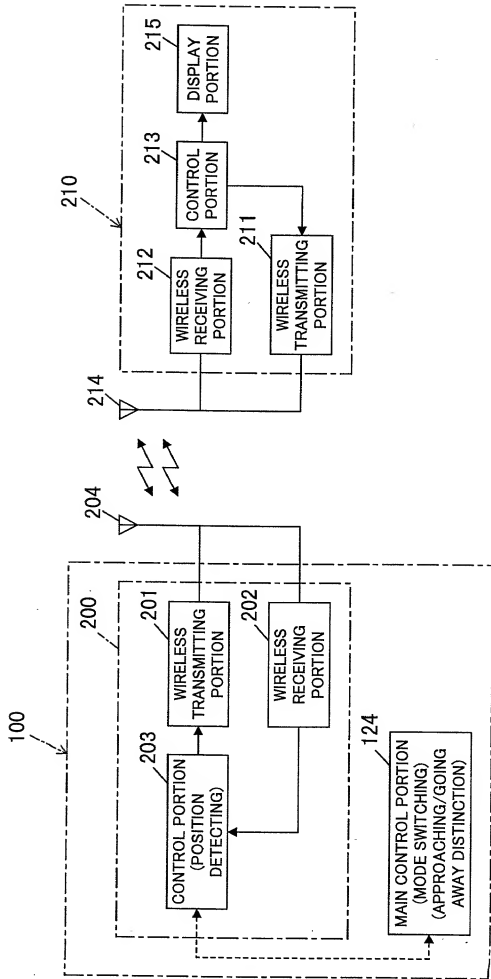


FIG. 4

```

graph LR
    1242[ROM] --> 1241[CPU]
    1243[NVRAM] <--> 1241
    200[COMMUNICATING PORTION] <--> 1241
    150[OPERATION PORTION CONTROLLER] <--> 1241
    151[OPERATION PANEL] <--> 150
    35[THERMISTOR] --> 1241
    1241 --> 32[HEATER]
    1241 --> 40[MOTOR]
  
```

FIG. 5

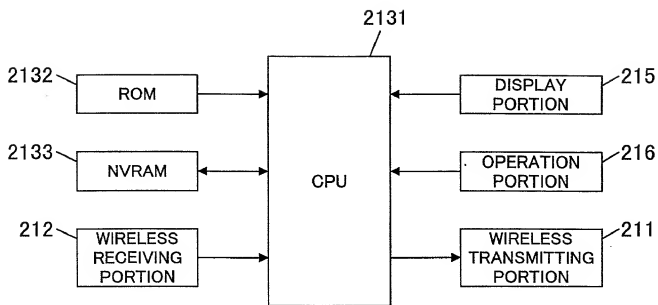


FIG. 6

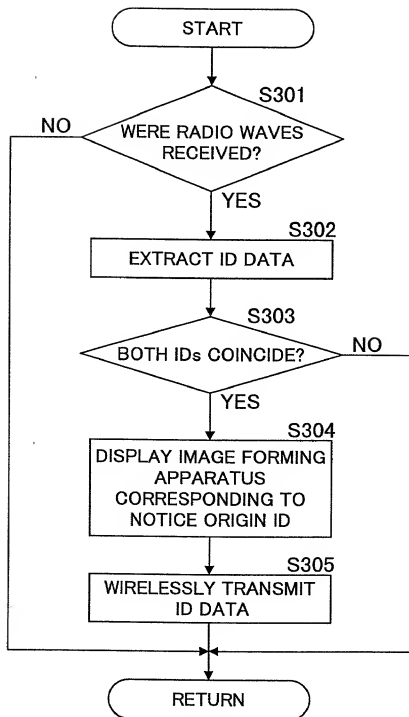


FIG. 7

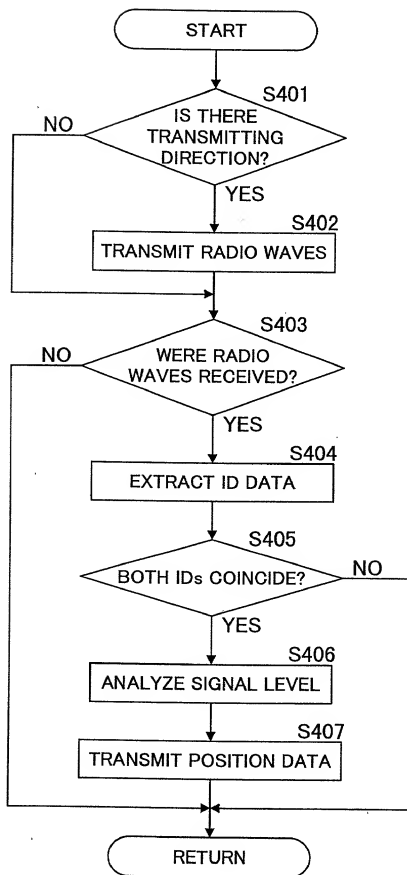


FIG. 8



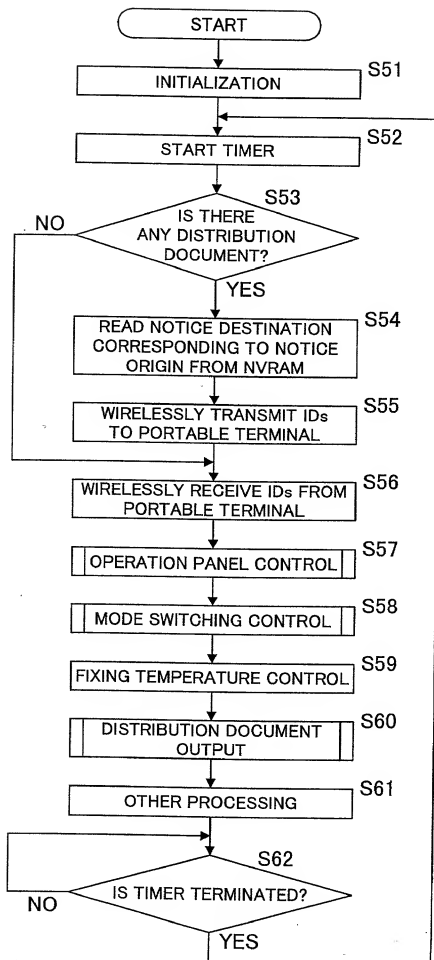


FIG. 9

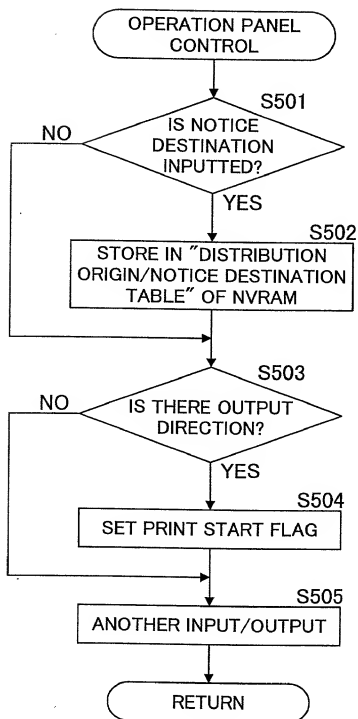


FIG. 10

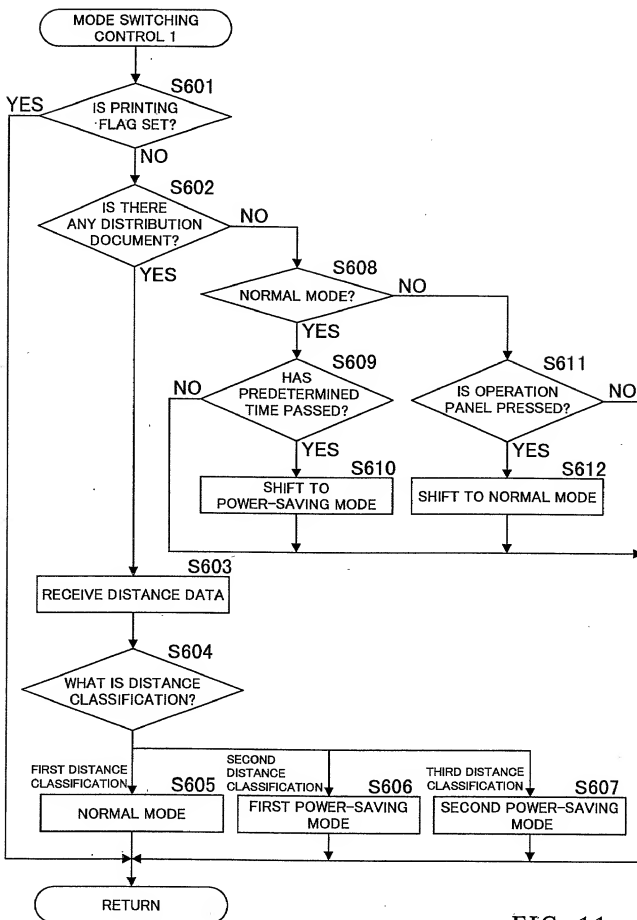


FIG. 11

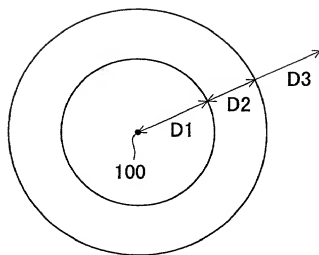


FIG. 12

0901022.071004

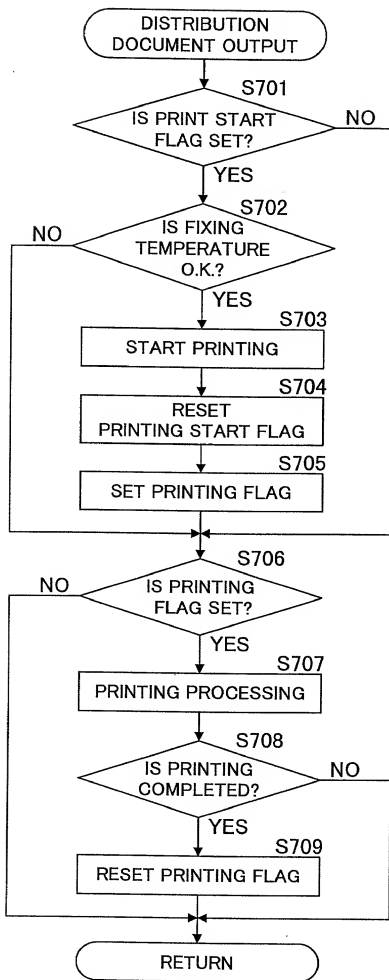


FIG. 13

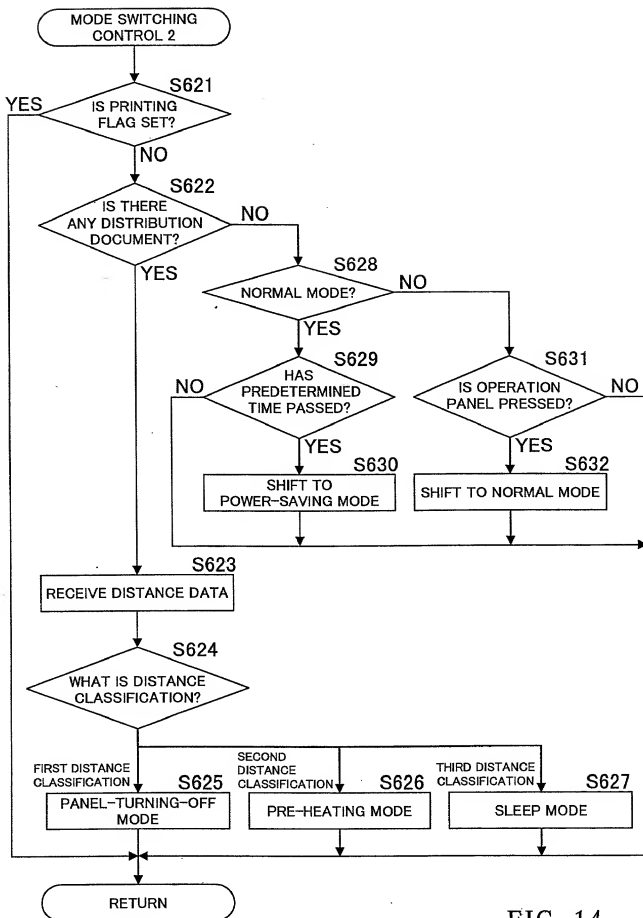


FIG. 14

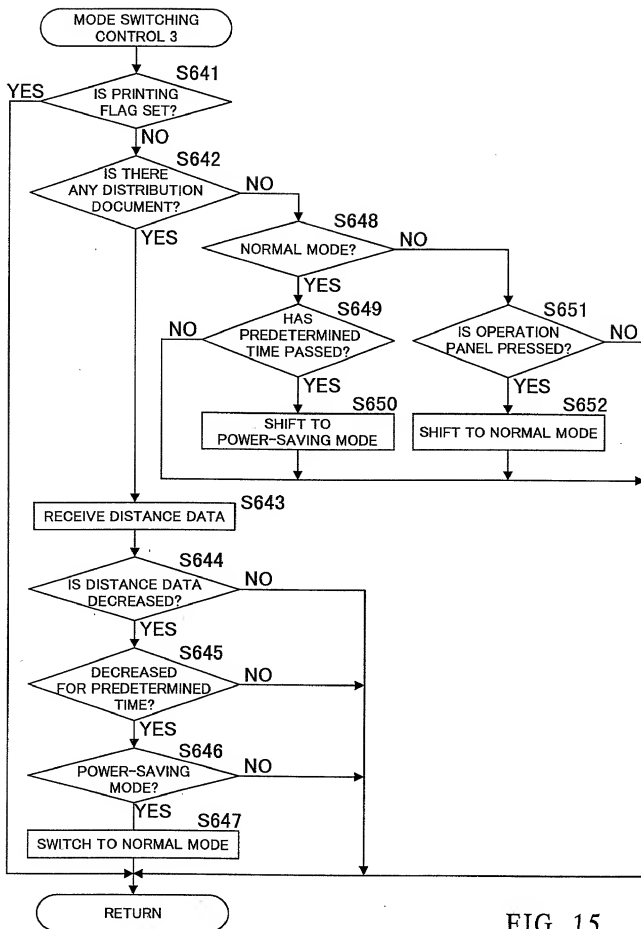


FIG. 15

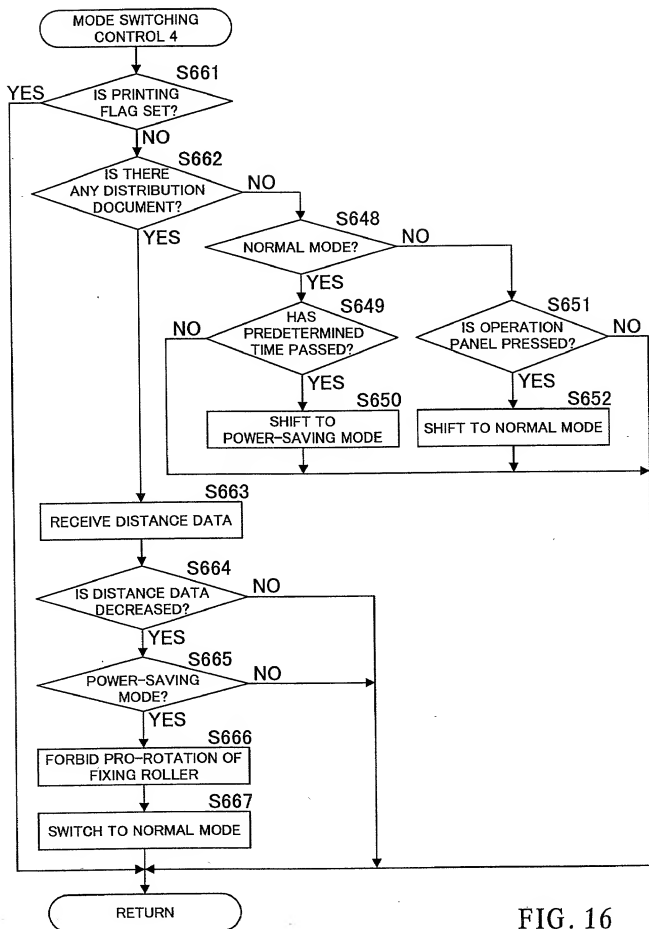


FIG. 16



00001032.074001

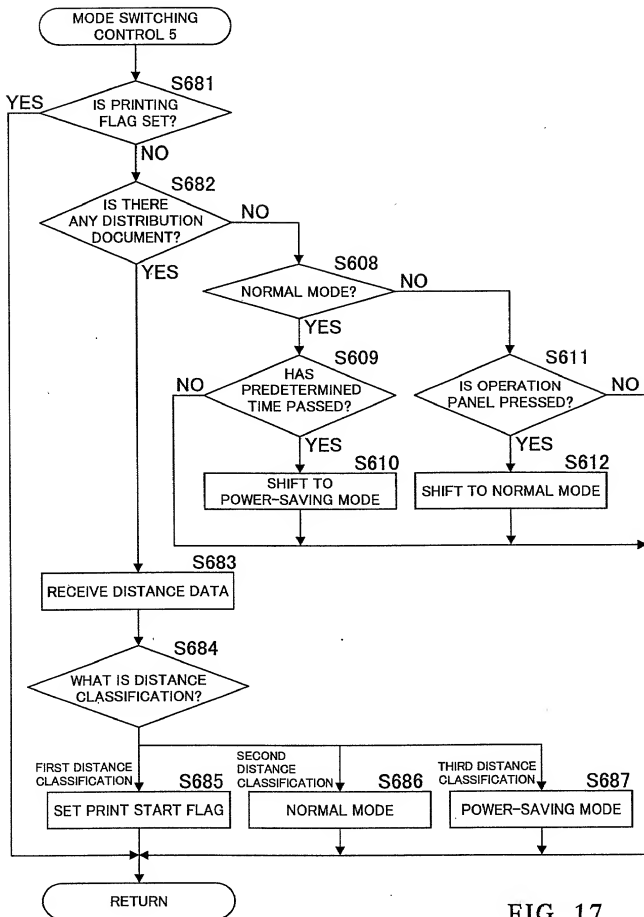


FIG. 17

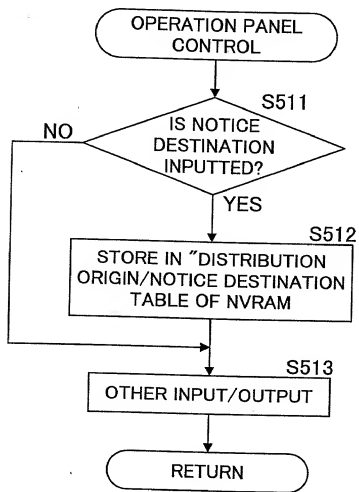


FIG. 18

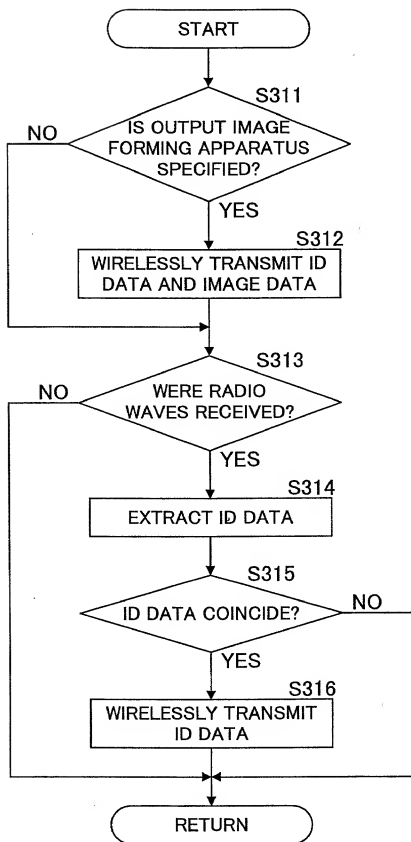


FIG. 19

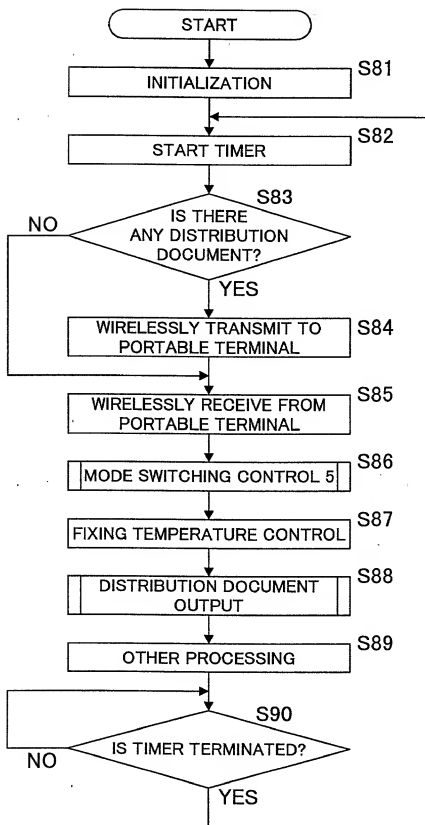


FIG. 20

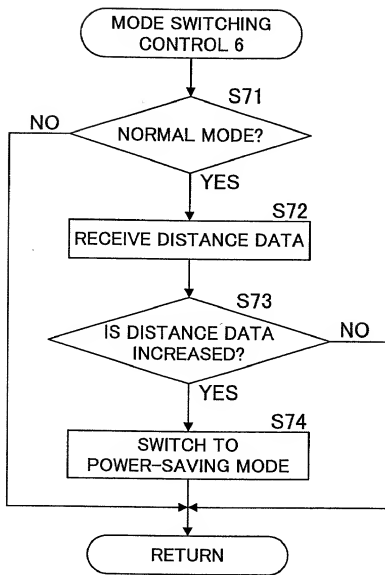


FIG. 21